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**history buffer**. o. uw. x . On a page hit, the page frequency. is incremented,  
the page is placed ... tivate the development of an **adaptive cache** replacement ...

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CLOCK across a wide-range of cache sizes and workloads. The algorithm CAR is inspired by the Adaptive Replacement Cache (ARC) algorithm, and inherits ...

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Clocks T1 and T2 contain cache pages. B1 and B2 contain recently evicted ...

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cache decay adaptive. Regardless of the initial state of the sys- ... clock cycles, and the output is the number of induced misses. in terms of cache lines. ...

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data by reducing cache miss through adaptive selection of. an index according to span direction ... running in much lower clock frequency than a CPU. Logic ...

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interval which corresponds to the number of clock cycles. between two consecutive cache misses. ... obtained using our adaptive prefetching method. The cache ...

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